

SPECIFICATION FOR TFT MODULE

MODULE No.: IPS080A101A

CUSTOMER APPROVAL:

| | SIGNATURE | DATE |
|-------------|-----------|------------|
| PREPARED BY | 余少伟 | 2018-02-28 |
| CHECKED BY | 73 3 | 2018-02-28 |
| APPROVED BY | Nota Liu | 2018-02-28 |

MODULE No. :IPS080A101A Rev No. : D P. 1



REVISION RECORD

| Rev No. | Rev date | Contents | Remarks |
|---------|------------|--|-------------|
| 0 | 2015-08-12 | First release | Preliminary |
| Α | 2016-05-13 | Modify the Interface Type | Page4 |
| В | 2016-06-28 | Add the typical value of LED life time | Page4 |
| С | 2017-09-26 | Update EXTERNAL DIMENSIONS | Page5 |
| D | 2018-02-28 | Update GENERAL INFORMATION | Page4 |
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1. GENERAL INFORMATION

| No. | Item | Contents | Unit |
|-----|--------------------------------|--------------------------------|-----------------|
| 1 | LCD size | 8.0 inch | 1 |
| 2 | LCD type | IPS/TRANSMISSIVE/ Normal Black | / |
| 3 | Viewing direction(eye) | FREE | 1 |
| 4 | Gray scale inversion direction | FREE | 1 |
| 5 | Resolution(H*V) | 800*3(RGB)*1280 | / |
| 6 | Module size (L*W*H) | 114.7*184.2*2.6 | mm ³ |
| 7 | Active area (L*W) | 107.64*172.22 | mm ² |
| 8 | Pixel pitch (L*W) | 0.13455*0.13455 | mm ² |
| 9 | Interface type | MIPI Interface | 1 |
| 10 | Module power consumption | TBD | w |
| 11 | Back light type | LED | 1 |
| 12 | Driver IC | HX8394-D | 1 |
| 13 | Weight | TBD | g |
| 14 | Display color | 16.7M | 1 |

2. ABSOLUTE MAXIMUM RATINGS

| Item | Symbol | Min. | Max. | Unit |
|--|--------|------|---------------|------|
| Power supply input voltage(TFT Module) | VDD | -0.3 | 3.6 | V |
| Backlight current (normal temp.) | ILED | - | 75 | mA |
| Operation temperature | Тор | -10 | 60 | °C |
| Storage temperature | Tst | -20 | 70 | °C |
| Humidity | RH | - | 90%(Max60 °C) | RH |

3. ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS(at Ta=25°C)

| Item | Symbol | Min. | Тур. | Max. | Unit | Note |
|--|--------|---------|------|------------------|------|------|
| Power supply input voltage(TFT Module) | VDD | 2.5 | 3.3 | 3.6 | V | |
| I/O logic voltage | VDDIO | - | - | | V | =VDD |
| Input voltage 'H' level | VIH | 0.8VDDI | - | VDDI | V | |
| Input voltage 'L' level | VIL | 0 | - | 0.2VDDI | V | |
| Power supply current | IVDD | - | TBD | : - 0 | V | |
| TFT gate on voltage | VGH | - | - | - | V | |
| TFT gate off voltage | VGL | - | - | ? — 1. | V | |
| Analog power supply voltage | AVDD | - | - | 5 | V | |
| Differential input common mode voltage | Vcom | - | - | (- | V | |

4. BACKLIGHT CHARACTERISTICS

(at Ta=25°C,RH=60%)

| Item | Symbol | Min. | Тур. | Max. | Unit | Note |
|-----------------------|--------|-----------------------------|-------|------|------|---------|
| LED forward voltage | VF | 21 | 22 | 23.3 | V | IF=60mA |
| LED forward current | IF | 1 | 60 | - | mA | |
| LED power consumption | PLED | .=> | 1.32 | - | W | Note1 |
| Number of LED | - | | 21 | | PCS | |
| Connection mode | = | 7 in series * 3 in parallel | | | / | |
| LED life-time | | 20000 | 30000 | Ξ | Hrs | Note2 |

Note1 : Calculator value for reference : IF*VF = PLED

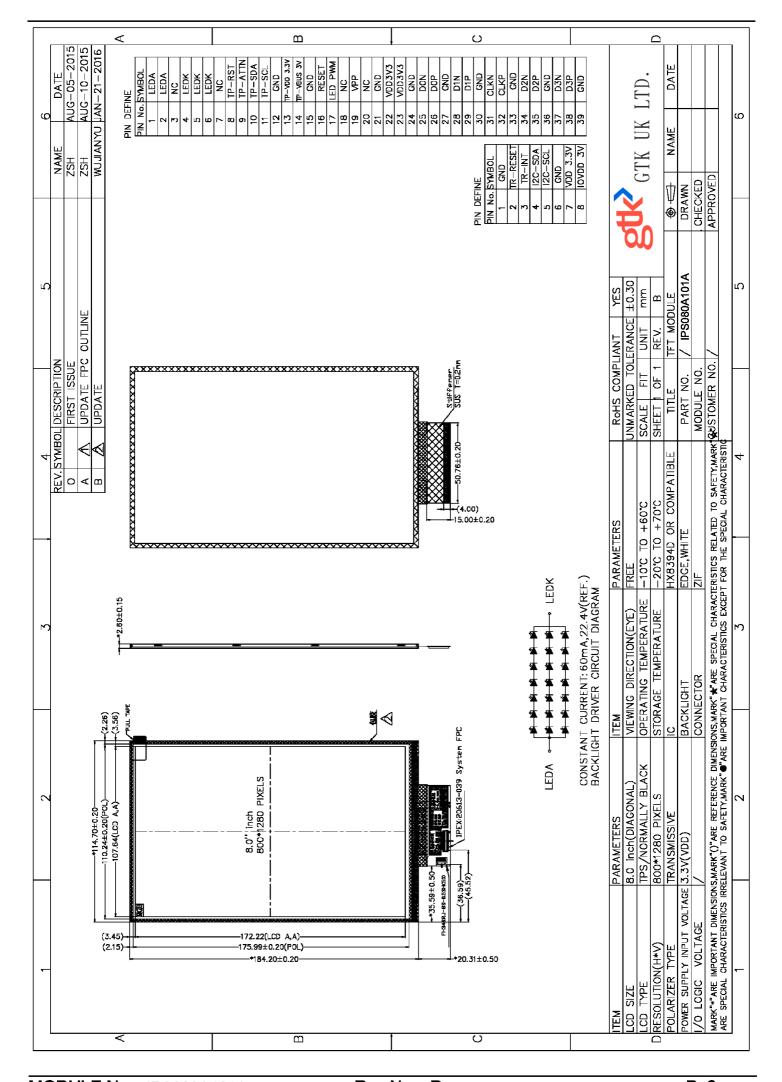
Note2: The LED life-time define as the estimated time to 50% degradation of initial brightness at Ta=25°C and IF =60mA. The LED lifetime could be decreased if operating IF is larger than 60mA.

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5. EXTERNAL DIMENSIONS

MODULE No. IPS080A101A





6. ELECTRO-OPTICAL CHARACTERISTICS

| Item | Symbol | Condition | Min. | Тур. | Max. | Unit | Remark | Note | |
|----------------------|---------|--------------|------|------|------|-------|---------|--------|--------|
| Response time | Tr+ Tf | | - | 30 | 40 | ms | FIG.1 | Note 4 | |
| Contrast ratio | Cr | - | 640 | 800 | - | | FIG.2 | Note 1 | |
| Surface luminance | Lv | θ=0° | 280 | 320 | - | cd/m2 | FIG.2 | Note 2 | |
| Luminance uniformity | Yu | θ=0° | 75 | 80 | - | % | FIG.2 | Note 3 | |
| NTSC | - | θ=0° | - | 60 | - | % | FIG.2 | Note 5 | |
| | | Ø=90° | 70 | 80 | - | deg | FIG.3 | Note 6 | |
| Viouing angle | θ | ∅=270° | 70 | 80 | - | deg | FIG.3 | | |
| Viewing angle | | wing angle | ∅=0° | 70 | 80 | - | deg | FIG.3 | Note 6 |
| | | ∅=180° | 70 | 80 | - | deg | FIG.3 | | |
| | Red x | | TBD | TBD | TBD | - | | | |
| | Red y | | TBD | TBD | TBD | - | | | |
| | Green x | | TBD | TBD | TBD | - | | | |
| CIE (x,y) | Green y | θ=0° ∅=0° | TBD | TBD | TBD | - | FIG.2 | Note 5 | |
| chromaticity | Blue x | | TBD | TBD | TBD | - | CIE1931 | Note 5 | |
| | Blue y | 10-20 0 | TBD | TBD | TBD | - | | | |
| | White x | | TBD | TBD | TBD | - | | | |
| | White y | | TBD | TBD | TBD | - | | | |

Note1. Definition of contrast ratio

Contrast ratio(Cr) is defined mathematically by the following formula.

For more information see FIG.2.

Contrast ratio= Luminance measured when LCD on the "White" state
Luminance measured when LCD on the "Black" state

Measured at the center area of the LCD

Note2.Definition of surface luminance

Surface luminance is the luminance with all pixels displaying white.

For more information see FIG.2.

Lv = Average Surface Luminance with all white pixels(P1,P2,P3,,Pn)

Note3.Definition of luminance uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

 $Y_u = \frac{\text{Minimum surface luminance with all white pixels (P1,P2,P3,....,Pn)}}{\text{Maximum surface luminance with all white pixels (P1,P2,P3,....,Pn)}}$

Note4. Definition of response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For additional information see FIG1.

Note5. Definition of color chromaticity (CIE1931)

CIE (x,y) chromaticity, The x,y value is determined by screen active area center position P5. For more information see FIG.2.

Note6. Definition of viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10. angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.3.

For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope or DMS series Instruments or compatible. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-5or BM-7 photo detector or compatible.



FIG.1. The definition of response Time

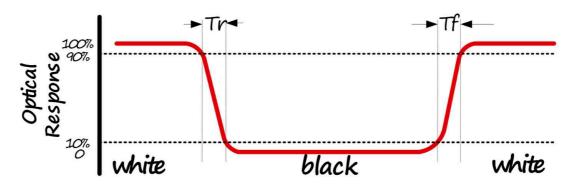


FIG.2. Measuring method for contrast ratio, surface luminance, luminance uniformity, CIE (x,y) chromaticity

Size : S≤5"(see Figure a) A : 5 mm B : 5 mm H,V : Active area

Light spot size \varnothing =5mm(BM-5) or \varnothing =7.7mm (BM-7)50cm distance or

compatible distance from the LCD surface to detector lens.

test spot position : see Figure a.

measurement instrument: TOPCON's luminance meter BM-5 or

BM-7 or compatible (see Figure c).

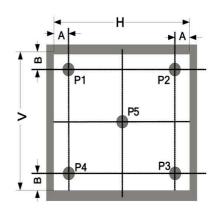


Figure a

Size: 5"<S≤12.3"(see Figure b)

H,V: Active area

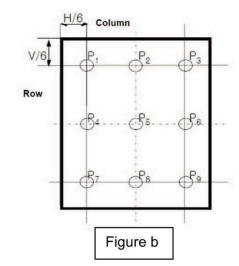
Light spot size ∅=5mm(BM-5) or ∅=7.7mm (BM-7)50cm distance or

compatible distance from the LCD surface to detector lens.

test spot position : see Figure b.

measurement instrument : TOPCON's luminance meter BM-5 or

BM-7 or compatible (see Figure c).



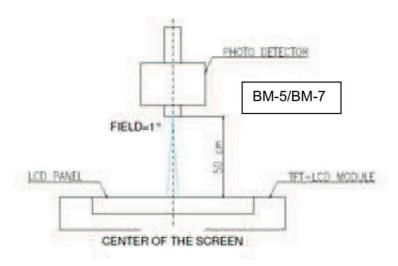
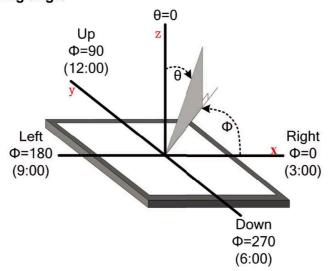


Figure c



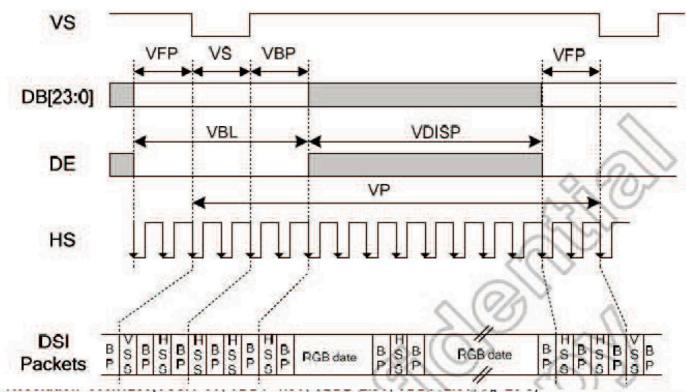
FIG.3. The definition of viewing angle





8. AC CHARACTERISTICS

8.1 Vertical timings



| Parameter | Symbol | Symbol Condition — | | Spec. | | | |
|---------------------------|--------|--------------------|---------------|-------|---------|------|--|
| raiailletei | Symbol | | | Тур. | Max. | Unit | |
| Vertical cycle | VP | 11 -40 | 1286 | -2 | | Line | |
| Vertical low pulse width | VS | | 2 | - | Note(1) | Line | |
| Vertical front porch | VFP | U FO) | 2 | | | Line | |
| Vertical back porch | VBP | 7/10 | 2 | - | Note(1) | Line | |
| Vertical data start point | 2VV- | VS+VBP | 4 | | Note(1) | Line | |
| Vertical blanking period | O VBL | VS+VBP+VFP | 6 | | | Line | |
| Vertical active area | -6 | VDISP | | 1280 | 7- | Line | |
| Vertical Refresh rate | VRR |) I | in a financia | 60 | - | Hz | |

Note: (1) The VS and VBP pulse width are related to GSP and GCK timing. The GSP and GCK must be set at corresponding position for LCD normal display.



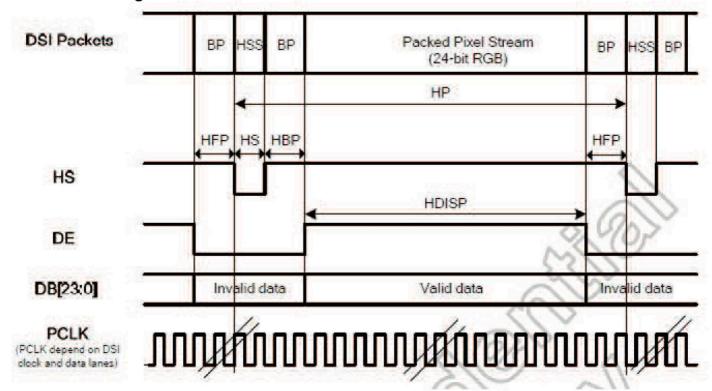
7. INTERFACE DESCRIPTION

TFT Module Interface description

| terface No. | Name | I/O or connect to | Description | |
|-------------|----------------|-------------------|--|--|
| 1-2 | LEDA | Р | Power for LED backlight (Anode) | |
| 3 | NC | 1- | Not connect | |
| 4-6 | LEDK | Р | Power for LED backlight (Cathode) | |
| 7 | NC | - | Not connect | |
| 8 | TP-RST | I | TP-RST | |
| 9 | TP-ATTN | I | TP-ATTN | |
| 10 | TP-SDA | I/O | TP-SDA | |
| 11 | TP-SCL | I | TP-SCL | |
| 12 | GND | Р | Power Ground | |
| 13 | TP-VDD 3.3V | Р | TP-VDD 3.3V | |
| 14 | TP-VBUS 3V | Р | TP-VBUS 3V | |
| 15 | GND | Р | Power Ground | |
| 16 | RESET | Ī | Global reset pin | |
| 17 | LED PWM | 0 | Backlight on/off control pin | |
| 18 | NC | - | Not connect | |
| 19 | VPP | 1 | External high voltage pin used in OTP mode | |
| 20 | NC | - | Not connect | |
| 21 | GND | Р | Power Ground | |
| 22-23 | VDD3V3 | Р | Power Voltage for digital circuit | |
| 24 | GND | Р | Power Ground | |
| 25 | D0N | I/O | MIPI data pair0 negative signal | |
| 26 | D0P | I/O | MIPI data pair0 Positive signal | |
| 27 | GND | Р | Power Ground | |
| 28 | D1N | I/O | MIPI data pair1 negative signal | |
| 29 | D1P | I/O | MIPI data pair1 Positive signal | |
| 30 | GND | Р | Power Ground | |
| 31 | CLKN | I/O | MIPI CLK negative signal | |
| 32 | CLKP | I/O | MIPI CLK Positive signal | |
| 33 | GND | Р | Power Ground | |
| 34 | D2N | I/O | MIPI data pair2 negative signal | |
| 35 | D2P | I/O | MIPI data pair2 Positive signal | |
| 36 | GND | Р | Power Ground | |
| 37 | D3N | I/O | MIPI data pair3 negative signal | |
| 38 | D3P | I/O | MIPI data pair3 Positive signal | |
| 39 | GND | Р | Power Ground | |



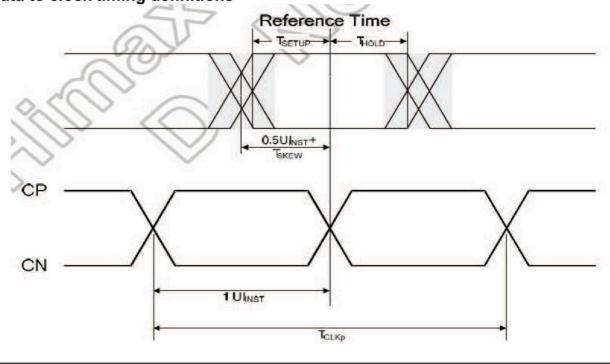
Horizontal timings



| Parameter | Symbol Condition | | | Unit | | |
|-----------------------------|------------------|------------|------|------|----------|-------------|
| rarameter | | | Min. | Тур. | Max. | Offic |
| HS low pulse width | HS | (V) . ((| 18 | - | | pclk(note1) |
| Horizontal back porch | HBP | (0) - (| 30 | - | | pclk(note1) |
| Horizontal front porch | HFP (| M | 10 | 31 | | pclk(note1) |
| Horizontal data start point | - (0 | HS+HBP | 38 | - | - | pclk(note1) |
| Horizontal blanking period | HBLK | HS+HBP+HFP | 48 | 1100 | | pclk(note1) |
| Horizontal active area | HDISP | (10) | | 800 | The same | DCK |

Note: (1) Depend on 550Mbps*4-Lane@60Hz

8.2 Data to clock timing definitions





9. POWER SEQUENCE

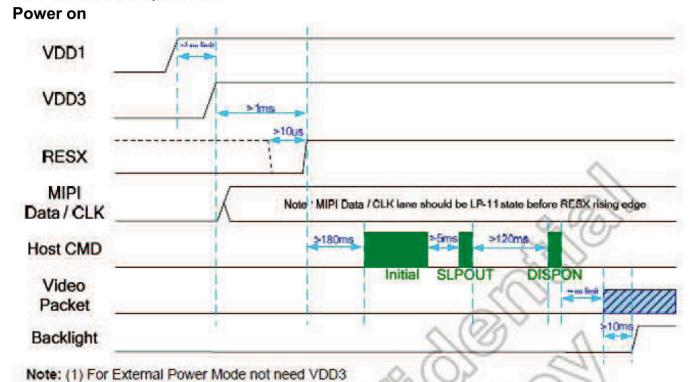
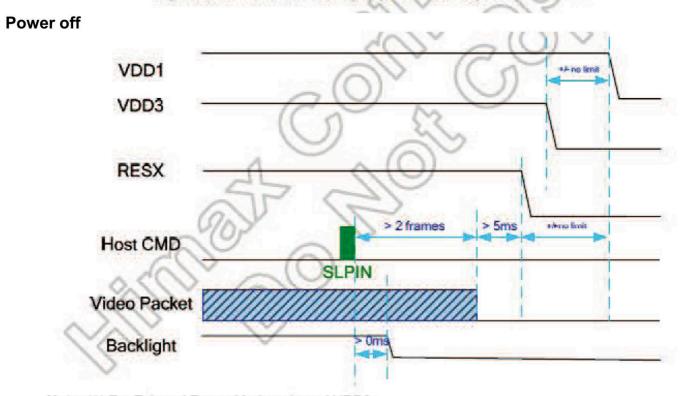


Figure 5.20: VDD1/VDD3 input power on sequence



Note: (1) For External Power Mode not need VDD3

Figure 5.21: VDD1/VDD3 input power off sequence



10. RELIABILITY TEST CONDITIONS

| No. | Test item | Test con | dition | Inspection after test |
|------|-------------------------------------|--|-----------------|--|
| 10.1 | High temperature storage test | +70°C/240 hours | | |
| 10.2 | Low temperature storage test | -20°C/240 hours | | |
| 10.3 | High temperature operating test | +60°C/120 hours | | |
| 10.4 | Low temperature operating test | -10°C/120 hours | | Inspection after |
| 10.5 | Temperature cycle storage test | -20°C ~ 25°C ~ +70° (30min.) (10min.) (30 | | 2~4hours storage at room temperature, the sample shall be free |
| 10.6 | High temperature high humidity test | +50°C*90% RH/120 hours | | from defects : 1.Current changing |
| 10.7 | Vibration test | Frequency: 250 r/min Amplitude: 1 inch Time: 45min | | value before test and after test is 50% larger; 2. Function defect : |
| | | Drop direction: 1 corner/3 edges/6 s | ides 10 time | Non-display,abnormal-d isplay,missing lines, Short lines,ITO |
| | | Packing weight(kg) | Drop height(cm) | corrosion; |
| 10.8 | Drop test | <11 | 80±1.6 | 3.Visual defect : Air bubble in the LCD,Seal |
| | | 11 ≦ G<21 | 60±1.2 | leak,Glass crack. |
| | | 21 ≦ G<31 | 50±1.0 | |
| | | 31 ≦ G<40 | 40±0.8 | |
| 10.9 | ESD test | Air discharge: ±8KV, 10time Contact discharge: ±4KV, 10time | | |

Remark:

- 1. The test samples should be applied to only one test item.
- 2.Sample size for each test item is 3~5pcs.
- 3. For High temperature high humidity test, Pure water(Resistance>10 $M\Omega$) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.B/L evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence B/L has.
- 6. Failure judgment criterion: Basic specification, Electrical characteristic, Mechanical characteristic, Optical characteristic.



11. INSPECTION CRITERION

11.1 Objective

The TFT test criterion are set to formalize TFT quality standards for GTK with reference to those of the customer for inspection, release and acceptance of finished TFT products in order to guarantee the quality of TFT products required by the customer.

11.2. Scope

The criterion is applicable to all the TFT products manufactured by GTK.

11.3. Equipment for Inspection

Electrical tester, electrical testing machines, vernier calipers, microscopes, magnifiers, anti-static wrist straps, finger cots, labels, tri-phase cold and hot shock machine, constant temperature and humidity chamber, backlight table, ovens for high-low temperature experiments, refrigerators, constant voltage power supply (DC), desk Lamps, etc.

11.4. Sampling Plan and Reference Standards

11.4.1 Sampling plan:

Refer to National Standard GB/T 2828.1---2012/ISO2859-1:1999, level II of normal levels :

Major defect: AQL 0.4 Minor defect: AQL 1.0

11.4.2 GB/T 2828.1---2012/ISO2859-1:1999 Sampling check procedure in count

11.4.3 GB/T 18910. Standard for LCM parts

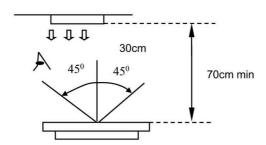
11.4.4 GB/T24213-2008 Basic Environmental Test Procedures for Electrical and Electronic Products

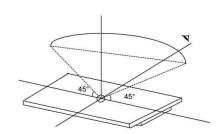
11.4.5 IPC-A-610E Acceptability of Electronic Assemblies

11.5. Inspection Conditions and Inspection Reference

11.5.1 Cosmetic inspection: shall be done normally at 23±5°C of the ambient temperature and 45~75%RH of relative humidity, under the ambient luminance between 500lux~1000lux and at the distance of 30cm apart between the inspector's eyes and the LCD panel and normally in reflected light. For backlight LCM, cosmetic inspection shall be done under the ambient luminance less than 100lux with the backlight on.

11.5.2 The TFT shall be tested at the angle of 45°left and right and 0-45° top and bottom as the following picture showing:





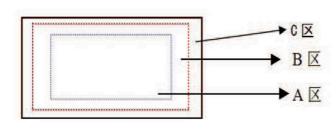
11.5.3 Definition of viewing area(VA)

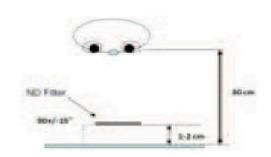
A area : Active area(AA area)
B area : Viewing area(VA area)

C area: Non-viewing area(not viewing after customer assembly)

If there is any appearance viewing defect which do not affect product quality and customer assembly in C area, it's accepted in generally.

The criteria apply to A and B area except chipping and crack.







- 11.5.4 Inspection with naked eyes(exclusive of the inspection of the physical dimensions of defects carried out with magnifiers)
- 11.5.5 ND card use method(refer to right conner image) and scope: Multi-bright dot; Mura(Black/Gray pattern uneven); dark line and so on.
- 11.5.6 Undefined items or other special items, refer to mutual agreement and limited sample. If criterion does not match product specifications/ technical requirement, both should be subject to special inspection criterion agreed by customer.

11.6. Defects and Acceptance Standards

- 11.6.1 Electrical properties test
- 11.6.1.1 Test voltage(V): Refer to the instruction of testers and the product specification or drawing and the display content and parameters and display effects shall conform to the product specification and drawing.
- 11.6.1.2 Current Consumption(I): Refer to approved product specifications or drawings.

11.6.1.3 Function items(Defect category : MA.)

| No. | Defects | Descriptions | Pictures | Inspection method/tools | Defect category |
|-------------|----------------------------------|---|------------------|-------------------------------|-----------------|
| 11.6.1.3.1 | 181 18 | shows no picture/display in normal connected situation. | | Naked eyes/ testers | MA. |
| 11.6.1.3.2 | Missing segment | Shows missing lines in normal display | | Naked eyes/ testers | MA. |
| 11.6.1.3.3 | Dark line | Only visible on gray pattern, 1 or more vertical/horizontal lines:5%ND,not visible,OK | 1 | Naked eyes/ testers | MA. |
| 11.6.1.3.4 | POL angle defect | Not accepted | 正常 POL贴反180度后 | Naked eyes/ testers | MA. |
| 11.6.1.3.5 | Image retention (sticking) | Chess pattern stays for 30mins and change to 50% gray pattern, disappear time <10s, OK; if time>10s, NG | | Naked eyes/ testers | MA. |
| 11.6.1.3.6 | Flicker | Refer to limit sample if essential or flicker value<-30dB(measured by CA310A); OK | | Naked eyes/ CA310A | MA. |
| 11.6.1.3.7 | Display abnormal | Not accepted | | Naked eyes/ testers | MA. |
| 11.6.1.3.8 | Cross-talk | Refer to limited sample | * | Naked eyes/ limited sample | MA. |
| 11.6.1.3.9 | Display dim/bright | Refer to limited sample | 1 | Naked eyes/ limited sample | MA. |
| 11.6.1.3.10 | Contrast | Refer to limited sample | 1 | Naked eyes/ limited sample | MA. |
| 11.6.1.3.11 | Huge current | Out of spec, not accepted | 1 | Ammeter | MA. |

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| | TP | | | Naked eyes/ | |
|-------------|----------|--------------|---|--------------|-----|
| 11.6.1.3.12 | function | Not accepted | / | Touch/ | MA. |
| | defect | | | test program | |

11.6.2 LCD dot/line defect

11.6.2.1 LCD pixel dot defect(defect category : MI.)

| Item | | Inspection criterio | n |
|---------------------------------|------|---------------------|-----------|
| Size | S<5" | 5"≤S<10" | 10"≤S<15" |
| Color pixel dot defect(RGB dot) | 1 | 2 | 2 |
| 2 connected bright dot | 0 | 1 | 1 |
| 3 connected bright dot or more | 0 | 0 | 1 |
| Bright dot quantity | 1 | 2 | 3 |
| Random dark dot quantity | 2 | 3 | 4 |
| 2 connected dark dot | 1 | 1 | 2 |
| 3 connected dark dot or more | 0 | 0 | 0 |
| Dark dot quantity | 3 | 4 | 5 |
| Multi-bright dot | | ND 3%hidden, OK | |

Remark: 2 bright dots distance DS≥15mm 2 dark dots distance DS≥5mm

- 1) Bright dot: Power on TFT and RGB dot in black display
- 2) Dark dot: Power on TFT and gray or black dot in RGB display
- 3) Multi-bright dot: Power on TFT and fluorescent tiny dot in black display(only visible in black display)

11.6.2.2 LCD appearance dot defect (defect category : MI.)

| No. | | | | spection c | | | Picture | Inspection |
|------------|-------------|---|---|-------------|--------------|---|-----------------|--------------|
| NO. | Item | Si | ze | S<5" | 5"≤S<10" | 10"≤S<15" | Picture | method/tools |
| | | D≤0 |).15 | Not count | Not count | D≤0.2mm | | |
| | | 0.15 <d≤0.25< td=""><td>3</td><td>3</td><td>Not count</td><td>↓b</td><td>Naked eyes</td></d≤0.25<> | | 3 | 3 | Not count | ↓ b | Naked eyes |
| | | 0.25< | D≤0.30 | 1 | 2 | 0.2~0.35mm | → a → | /film card |
| | Dot defect | 0.30< | 0.30 <d≤0.35< td=""><td>1</td><td>Q'ty ≤ 4</td><td></td><td>/magnifier</td></d≤0.35<> | | 1 | Q'ty ≤ 4 | | /magnifier |
| 11.6.2.2.1 | (black dot, | 0.35< | D≤0.50 | 0 | 0 | 1 | D=(a+b)/2 | /magniner |
| | white dot) | | 0.5 | 0 | 0 | 0 | | |
| | | | | | nt.Multi-dot | as bulk is not | accepted. | |
| | | Count do | | | | | | |
| | | 2 round d | ots or line | ar dots in | 1 cm is judo | ged as multi-d | ot. | |
| | | Length | Width | S<5" | 5"≤S<10" | 10"≤S<15" | | |
| | | (mm) | (mm) | | 0 = 0 1.0 | | | |
| | | Not | W≤0.03 | Accepted | Accepted | Accepted | h= | |
| | | count | | | | ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,, | | |
| | | L≤5 | 0.03≤W | 3 | 3 | Not count | " | Naked eyes |
| | Line | | <0.05 | | | | | /film card |
| | defect | L≤5 | 0.05≤W | 0 | 1 | 3 | | /magnifier |
| 11.6.2.2.2 | (visible | | <0.08 | | | | 1 | |
| | when | L≤8 | 0.05≤W <0.08 | 0 | 0 | 1 | | |
| | power on) | L>8 | <0.08 W>0.08 | 0 | | | 1_+ | |
| | | ***** | VV>0.06 | U | | | | |
| | | Remark : | | | | | | |
| | | | | | | pecial angle a | | I |
| | | | K/folding/s | scratch but | can not be | touched, no | control or refe | r to keeping |
| | | sample. | | | | | | |



| | Polarizer | Size(mm) | S<5" | 5"≤S<10" | 10"≤S<15" | | |
|------------|------------------------|---|-----------|-----------|-----------|------------|------------|
| | convex- | D≤0.20 | Not count | Not count | Not count | | |
| | concave dot defect, | 0.20 <d≤0.5< td=""><td>2</td><td>2</td><td>3</td><td>Naked eyes</td></d≤0.5<> | 2 | 2 | 3 | Naked eyes | |
| 11.6.2.2.3 | | 0.50 <d≤0.8< td=""><td>0</td><td>1</td><td>3</td><td>\$ tb</td><td>/film card</td></d≤0.8<> | 0 | 1 | 3 | \$ tb | /film card |
| | polarizer | 0.8 <d≤1.5< td=""><td>0</td><td>0</td><td>1</td><td>+ a →</td><td>/magnifier</td></d≤1.5<> | 0 | 0 | 1 | + a → | /magnifier |
| | bubble defect | D>1.5mm | 0 | 0 | 0 | | |

| No. | Item | | Accepte | d criterion(mm) | | MA. | MI. |
|----------|--|--------------------------|-----------------------------|--|---|-----|----------|
| 11.6.3.1 | ITO conductive side | Х | 1 | ≤1/8L | 1 | | |
| | | Y | Y≤1/6W | 1/6W <y≤1 4w<="" td=""><td>1/4W <y< td=""><td></td><td>ī</td></y<></td></y≤1> | 1/4W <y< td=""><td></td><td>ī</td></y<> | | ī |
| | XXXXXX | Accept | 2 | 2 | 0 | | V |
| | Corner chipping | X | 1 | ≤1/6L | 1 | | √ |
| | (ITO pins position) | Υ | Y≤1/2W | 1/2W <y≤w< td=""><td>W <y< td=""><td></td><td>V</td></y<></td></y≤w<> | W <y< td=""><td></td><td>V</td></y<> | | V |
| 11.6.3.2 | | Accept | 2 | 1 | 0 | | |
| | Z | per 6.3.3; black bord | at the same er of the fr | red in sealed edge e time it should no ame and the corn ection position per | er chipping | | |
| | Chipping in sealed area (outside chipping) | Х | 1 | ≤1/8L | 1 | | |
| | J. J. | Y(outside chipping) | Not enter | Enter Y≤H | H <y< td=""><td></td><td></td></y<> | | |
| | X Y | Y(inside chipping) | into sealant | Enter Y≤1/2H | 1/2H <y< td=""><td></td><td></td></y<> | | |
| 11.6.3.3 | 27 | Z | ≤T | ≤1/2T | 1 | | |
| | 12 | Accept | 2 | 1 | 0 | | |
| | Chipping in sealed area (inside chipping) | sealing are in the oppo | a are same site of stage | r and outer chippi . When the chippi e, Y as per the chip andard in 6.3.1 | ng occurred | | |
| | Conductive side (back side chipping) | Х | 1 | ≤1/6L | 1 | | |
| 11.60.4 | ,y | Υ | Y≤1/3W | 1/3W <y≤2 3w<="" td=""><td>2/3W <y< td=""><td></td><td>√</td></y<></td></y≤2> | 2/3W <y< td=""><td></td><td>√</td></y<> | | √ |
| 11.6.3.4 | Z | Accept | 2 | 2 | 0 | | |
| | , x, | Chipping in | to ITO side, | refer to 6.3.1 | | | |



| | Protruding LCD poor cutting and LCD burrs | X | / | ≤1/8L | / | |
|----------|---|----------------------|--------------|--|----------------------------------|-----|
| | cutting and LOD barrs | Y | ≤1/6W | 1/6W <y≤1 5w<="" td=""><td>1/5W <y< td=""><td>ا ا</td></y<></td></y≤1> | 1/5W <y< td=""><td>ا ا</td></y<> | ا ا |
| 11.6.3.5 | , b | Z | 1 | 1 | / | v |
| | | Accept | 1 | 1 | 1 | |
| | | The outside drawing. | e protruding | control as per the t | tolerance of | |
| 11.6.3.6 | Crack | expand to i | nside is NG | ks without direction , but to outside is C amaged standard) | K | √ |

Remark:

X means the length of chipping;

Y means the width;

Z means the thickness;

W means the step width of the two glasses;

H means the distance from the glass edge to the sealant inner edge;

T means glass thickness.

11.6.4 Backlight components

| No. | Item | Description | Accepted criterion | MA. | MI. |
|----------|--------------------------|---|--------------------------------|-----|----------|
| 11.6.4.1 | No backlight wrong Color | 1 | Rejected | √ | |
| 11.6.4.2 | Color deviation | When powered on, the LCD color differs from its sample and found that the color not conforming to the drawing after testing. | Refer to sample and drawing | | V |
| 11.6.4.3 | Brightness deviation | When powered on, the LCD brightness differs from its sample and is found after testing not conforming to the drawing; or if it conforms to the drawing but the brightness over ±40% than its typical value. | Refer to sample and drawing | | ~ |
| 11.6.4.4 | Uneven brightness | Uneven on the same LCD and out of the specification of the drawing. The no specification evenness= (the max value-the min value)/ mean value< 70%. | Refer to sample and drawing | | V |
| 11.6.4.5 | Spot/line/ scratch | When power on, it has dirty spot, scratches and so on spot and line defects. | Refer to 6.2.2 | | V |

11.6.5 Metal frame (Metal Bezel)

| No. | Item | Description | Accepted criterion | MA. | MI. |
|----------|--|---|--------------------|-----|-----|
| 11.6.5.1 | Material & surface treatment | Metal frame/surface treatment do not conform to the specifications. | Rejected | V | |
| 11.6.5.2 | Tab twist Unconformity /Tab not twisted | Wrong twist method or direction and twist tabs are not twisted as required. | Rejected | V | |



| 11.6.5.3 | Bezel paint loss | 1.Front surface : Paint peel off and scratch to the | | V |
|----------|---|--|----------|----------|
| 11.6.5.4 | Bezel scratch | bottom Dot:D≤0.5mm, exceeds 3; | | √ |
| 11.6.5.5 | Painting peel off, discoloration, dent, and scratch | Line:L≤3.0mm,W≤0.05mm exceeds 2; 2.Front dent, air bubble and side with paint peeling off scratch to the bottom Dot: D≤1.0mm, exceeds 3; Line:L≤3.0mm,W≤0.05mm, exceeds 2; | Rejected | V |
| 11.6.5.6 | Burr | Burr(s) on metal bezel is so long as to get into viewing area. | Rejected | V |

11.6.6 FPC

| No. | Item | Description | Accepted criterion | MA. | MI. |
|----------|--------------------------------|--|--|----------|----------|
| 11.6.6.1 | Model &P/N | Material model & P/N | Keep the same with drawing and technical requirement | √ | |
| 11.6.6.2 | Dimension/ position | Dimension in drawing spec H | f≤1/3w, h ≤1/3H, dimension in drawing spec-> OK Conducive material and ITO/PDA connective area must over than 1/2. Entire dimension must be in spec tolerance. | | V |
| 11.6.6.3 | FPC appearance | Hot pressing material get broken, folding line open; FPC golden finger oxidate, broken ,scratch ,foreign material which cause line short | Broken length<2mm; FPC line is OK- > Accepted Crack and line broken->Rejected | | √ |
| 11.6.6.4 | FPC burr | Burr near FPC edge area | When cover line and burr length ≤1.0mm->Accepted | | √ |
| 11.6.6.5 | FPC falling off | FPC bonding area falling off; silica gel breaking | Rejected | | V |
| 11.6.6.6 | Sealant missing ITO line | Sealant is not covered all ITO line | Rejected | V | |
| 11.6.6.7 | Missing sealant | No sealant | Rejected | V | |
| 11.6.6.8 | Sealant | Sealant height ->product total height | Rejected | √ | |



11.6.7 SMT

| No. | Item | Description | Accepted criterion | MA. | MI. |
|----------|------------------------|---|--------------------|-----|----------|
| 11.6.7.1 | Soldering bridge | Solder between adjacent pads and components | Rejected | | V |
| 11.6.7.2 | Solder ball/splash | Solder ball/tin dross causing short circuit at the solder point. There are active solder ball and splash. | Rejected | | V |
| 11.6.7.3 | Soldering excursion | Soldering slant > 1/3 soldering pad 「學生養養度 | Rejected | | V |
| 11.6.7.4 | Component wrong | Component on PCB differs with drawing: wrong one, extra one,lack one,opposite polarity | Rejected | V | |
| | attaching | JUMP short circuit on PCB: extra soldering, lack soldering. | Rejected | √ | |
| 11.6.7.5 | Component falling off | Soldering but component is missing | Rejected | √ | |
| 11.6.7.6 | Wrong component | Component model/spec differs from product specification | Rejected | √ | |

11.6.8 General Appearance

| No. | Item | Description | Accepted criterion | MA. | MI. |
|----------|---------------------------------|--|--|--------------|----------|
| 11.6.8.1 | Dimension | According to drawing | Accepted | √ | |
| 11.6.8.2 | Surface stain | Defect mark or label are not removed residual glue, and finger print,etc; | Rejected | | V |
| 11.6.8.3 | Assembly foreign material | Dot/linear stain after assembly backlight and diffuse film TP assembly fogy stain | Invisible when power on->OK Refer to 6.2.2 dot/line spec | | √ |
| 11.6.8.4 | Mixture | Different model product in the same shipment | Rejected | \checkmark | |
| 11.6.8.5 | Product mark | Missing, unclear, incorrect, or misplaced part | Rejected | | V |
| 11.6.8.6 | Componen t mark | Silk screen mark clear, resistance measured value in spec | Accepted (Refer to customer special requirement) | | √ |
| 11.6.8.7 | Newton's rings | Area<1/6 screen area quantity≤1 | Accepted | | V |
| 11.6.8.8 | Mura | 1.In black display ND 3% invisible ->OK; visible->NG 2.Naked eyes inspection RGB display invisible Black display, area<1/4 screen area | Refer to limited sample | | V |



| 11.6.8.9 | Light leak | 1.LCD edge(near backlight) shadow by LCD lamps irregular illuminate 2.Judge in black/white/gray display (slight leaky is yellowish,greenish, blueish ->NG); Tape 浮起漏光 | Refer to limited sample | √ |
|-----------|------------|--|-------------------------|----------|
| 11.6.8.10 | Polarizer | 1.Polarizer slant.Cover VA and not over LCD edge2.No unmovable stain or finger print in polarizer VA3.Bubble/warped but not enter VA | Accepted | √ |
| 11.6.8.11 | TP defect | 1.TP crack 2.TP stain(fogy& unremovable) 3.TP glue overflow to VA | Rejected | V |

Remark:

Anything which is not clearly defined in 6.5~6.8 should refer to IPC-A-610E.Consumer Electronics, Non-consumer Electronics refer to I grade and Industrial, Automobile refer to II grade.

11.7 Others

Items not specified in this document or released on compromise should be inspected with reference to mutual agreement and limit samples.



12. HANDLING PRECAUTIONS

12.1 Mounting method

The LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly:

- .lsopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- .Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated :

- Soldering flux
- •.Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

12.4 Packing

Module employ LCD elements and must be treated as such.

- Avoid intense shock and falls from a height.
- •. To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

12.5 Caution for operation

- •.It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- •.An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- •.Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- •.If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- •.A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
- •. Usage under the maximum operating temperature, 50%Rh or less is required.
- •.When fixed patterns are displayed for a long time, remnant image is likely to occur.

12.6 Storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storing in an ambient temperature 10°C to 30°C, and in a relative humidity of 45% to 75%. Don't expose to sunlight or fluorescent light.
- •. Storing in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- •.Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- •. Storing with no touch on polarizer surface by the anything else.

It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

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12.7 Safety

- •.It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- •. When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

13. PRECAUTION FOR USE

- **13.1** A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- **13.2** On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.
- •. When a question is arisen in this specification.
- •. When a new problem is arisen which is not specified in this specifications.
- •. When an inspection specifications change or operating condition change in customer is reported to AVD, and some problem is arisen in this specification due to the change.
- •. When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. PACKING SPECIFICATION

Please consult our technical department for detail information.

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